# Traffic Intersection VHDL project

|  |  |  |  |
| --- | --- | --- | --- |
| **EEE20001 Digital Electronics Design** | | | |
| **Lab Day**  **(Wed etc.)** |  | **Lab Time**  **e.g. 12:30 pm** |  |
| **Name of Lab supervisors** | |  | |

|  |  |  |
| --- | --- | --- |
| **Student information (please print)** | | |
| **Family Name** | **Given Name** | **Student ID** |
|  |  |  |

|  |
| --- |
| DECLARATION AND STATEMENT OF AUTHORSHIP |
| 1. We have not impersonated, or allowed ourselves to be impersonated by any person for the purposes of this assessment. 2. This assessment is our original work and no part of it has been copied from any other source except where due acknowledgement is made. 3. No part of this assessment has been written for us by any other person except where such collaboration has been authorised by the lecturer concerned. 4. I have not previously submitted this work for this or any other course/unit. 5. I give permission for my assessment response to be reproduced, communicated, compared and archived for plagiarism detection, benchmarking or educational purposes.   I understand that:   1. Plagiarism is the presentation of the work, idea or creation of another person as though it is your own. It is a form of cheating and is a very serious academic offence that may lead to exclusion from the University. Plagiarised material can be drawn from, and presented in, written, graphic and visual form, including electronic data and oral presentations. Plagiarism occurs when the origin of the material used is not appropriately cited.   **Student signature/s**  I declare that we have read and understood the declaration and statement of authorship.  (sign or type your name here) |
| Further information relating to the penalties for plagiarism, which range from a formal caution to expulsion from the University is contained on the Current Students website at **www.swin.edu.au/student/** |

Synthesis Summary (from ISE Summary report)

**Number of Latches:** \_\_\_\_\_\_

**Macrocells Used:** \_\_\_\_\_\_/64 (\_\_\_%)

**Pterms Used:** \_\_\_\_\_\_/224 (\_\_\_%)

**Registers Used** \_\_\_\_\_\_/64 (\_\_\_%)